Notice of References Cited Application/Control No. | Applicant(s)/Patent Under | Reexamination | YIN ET AL. | Examiner | Art Unit | Michael B. Shingleton | 2817 | Page 1 of 1

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-35301 123	07/1996	Davis, Jr.	331/65
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Cho "Digitally - Controlled PLL with Pulse Width Detection Mechanism for Error Correction" IEEE International Solid State Circuits Conference 1997 Digest of Technical papers Feb 6-8, 1997 pp 334-335" + 481
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